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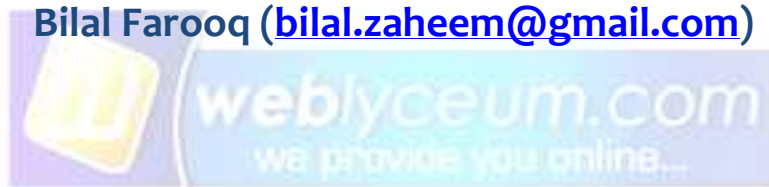
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Rana Muhammad Safdar ([rana.sfdr@gmail.com](mailto:rana.sfdr@gmail.com))

Bilal Farooq ([bilal.zaheem@gmail.com](mailto:bilal.zaheem@gmail.com))



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Collected and Composed by Bilal Farooq ([bilal.zaheem@gmail.com](mailto:bilal.zaheem@gmail.com))

# FINAL TERM EXAMINATION

SPRING 2010

CS401 => 9 AUG 2010

Question No: 1 ( Marks: 1 ) - Please choose one

When a 32 bit number is divided by a 16 bit number, the quotient is of

- 32 bits
- 16 bits
- 8 bits
- 4 bits

Question No: 2 ( Marks: 1 ) - Please choose one

In the instruction MOV AX,5 the number of operands are

- 1
- 2
- 3
- 4

Question No: 3 ( Marks: 1 ) - Please choose one

3. In DOS input buffer , number of characters actually read on return is stored in

- First byte
- Second byte
- Third byte
- Fourth byte

Question No: 4 ( Marks: 1 ) - Please choose one

7. VESA VEB 2.0 is standard for

- High Resolution Mode
- Low Resolution Mode
- Very High Resolution Mode
- Medium Resolution Mode

Question No: 5 ( Marks: 1 ) - Please choose one

22. IN DB-9 connector the Data Set ready pin is at

- 5
- 6
- 7
- 8

Question No: 6 ( Marks: 1 ) - Please choose one

Threads can have function calls, parameters and \_\_\_\_\_ variables.

- global

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- local
- legal
- illegal

Question No: 7 ( Marks: 1 ) - Please choose one  
How many prevalent calling conventions do exist

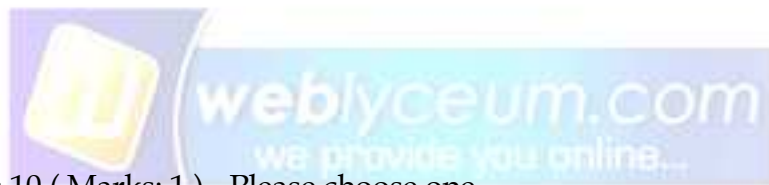
- 1
- 2
- 3
- 4

Question No: 8 ( Marks: 1 ) - Please choose one  
In 9pin DB 9 DSR is assigned on pin number

- 4
- 5
- 6
- 7

Question No: 9 ( Marks: 1 ) - Please choose one  
In 9pin DB 9 CTS is assigned on pin number

- 6
- 7
- 8
- 9



Question No: 10 ( Marks: 1 ) - Please choose one  
In 9pin DB 9 CD is assigned on pin number

- 1
- 2
- 3
- 4

Question No: 11 ( Marks: 1 ) - Please choose one

A 32bit address register can access upto ...access has .of memory so memory increased a lot.

- 2GB
- 4GB
- 6GB
- 8GB

Question No: 12 ( Marks: 1 ) - Please choose one in device attribute word which of the following bit decides whether it is a character device or a block device

- Bit 12
- Bit 13
- Bit 14
- Bit 15

Question No: 13 ( Marks: 1 ) - Please choose one

9. Which of the following IRQ is cascading interrupt

- IRQ 0
- IRQ 1
- IRQ 2
- IRQ 3

Question No: 14 ( Marks: 1 ) - Please choose one

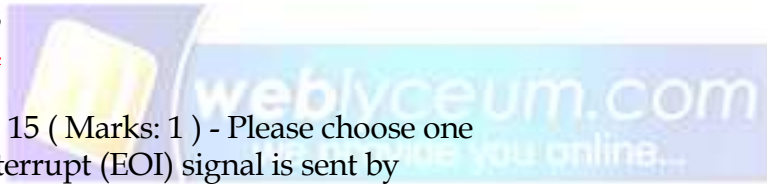
Which of the following interrupts is used for Arithmetic overflow

- INT 1
- INT 2
- INT 3
- INT 4

Question No: 15 ( Marks: 1 ) - Please choose one

An End of Interrupt (EOI) signal is sent by

- Handler
- Processor



- IRQ
- PIC

Question No: 16 ( Marks: 1 ) - Please choose one  
The number of pins in a parallel port connector are?

- 20
- 25
- 30
- 35

Question No: 17 ( Marks: 1 ) - Please choose one  
Which of the following pins of a parallel port connector are grounded?

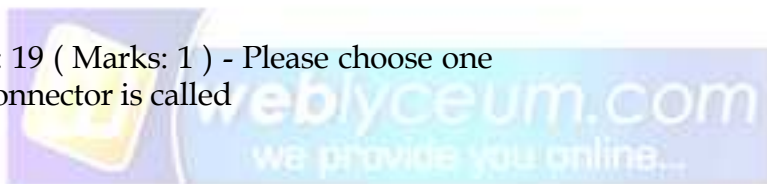
- 10-18
- 18-25
- 25-32
- 32-39

Question No: 18 ( Marks: 1 ) - Please choose one  
A 32bit address register can access upto ... .. of memory so memory access has increased a lot.

- 2GB
- 4GB
- 6GB
- 8GB

Question No: 19 ( Marks: 1 ) - Please choose one  
9 Pin Serial connector is called

- DB-7
- DB-9
- DB-25
- 9DB-5



Question No: 20 ( Marks: 1 ) - Please choose one In  
NASM an imported symbol is declared with the ... . while and  
exported symbol is declared with the ...

- Global directive, External directive
- External directive, Global directive
- Home Directive, Foreign Directive
- Foreign Directive, Home Directive

Question No: 21 ( Marks: 2 )  
Write brief about INT 13 - Extended READ SERVICES

Question No: 22 ( Marks: 2 )  
What is Interrupt flag?

Question No: 23 ( Marks: 3 )  
Give the name of any two descriptors

Question No: 24 ( Marks: 3 )  
It is the part of Multitasking TSR caller, what will do these instructions comment against them (3)

```
Mov al, [chars+bx]
Mov [es:40],al
Inc bx
```

Question No: 25 ( Marks: 5 )

Write Data Movement and Arithmetic Instructions of Motorola 68 K Processor.

Question No: 26 ( Marks: 5 )

Write assembly program for “Break Interrupt Service Routine”

